

(LCR), as well as comprehensive physical insight. A comparison with L-band experimental data has shown very good agreement. This new method is believed to be a useful complement to system designers in the fields of fade mitigation techniques and diversity signal processing.

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0.25 μm gate-length, MBE-grown AlGaIn/GaN HEMTs with high current and high f_T

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MBE-grown AlGaIn/GaN high electron mobility transistors (HEMTs) on sapphire substrates have been fabricated. These 0.25 μm gate-length devices exhibited a maximum drain current density as high as 1.39 A/mm, a unity gain cutoff frequency (f_T) of 67 GHz, and a maximum frequency of oscillation (f_{max}) of 136 GHz. The f_T of 67 GHz and f_{max} of 136 GHz are the highest reported values for 0.25 μm gate-length GaN-based HEMTs.

GaN-based high electron mobility transistors (HEMTs) are promising devices for high power and high temperature applications [1–3]. This potential is due to advantageous material properties such as a wide bandgap leading to high breakdown voltage, a high saturated-electron drift velocity and the existence of AlGaIn/GaN heterostructures with a high conduction band offset and high piezoelectricity resulting in high sheet carrier densities in the 10^{13} cm^{-2} range. Recently, tremendous progress has been recorded in the material quality and device processing of GaN-based HEMTs. This has resulted in significant improvement in the DC and RF performances of these devices. AlGaIn/GaN HEMTs with a current density as high as 1.7 A/mm have been reported [4]. 50 nm gate-length AlGaIn/GaN HEMTs with a record unity gain cutoff frequency (f_T) 110 GHz and maximum frequency of oscillation (f_{max}) over 140 GHz have been demonstrated [5].

In this Letter, we report the simultaneous achievement of the high current density and high cutoff frequencies for 0.25 μm gate-

length AlGaIn/GaN HEMTs on sapphire substrates grown by molecular beam epitaxy (MBE). The current density of 1.39 A/mm, f_T of 67 GHz and f_{max} of 136 GHz represent significant improvements for AlGaIn/GaN HEMTs of similar gate-lengths on sapphire substrates.

The layer used here was grown on sapphire substrates by molecular beam epitaxy (MBE). The epilayer consists of 2 μm undoped GaN, 30 nm undoped $\text{Al}_{0.24}\text{Ga}_{0.76}\text{N}$ and a 5 nm undoped GaN cap layer. Hall measurements showed a sheet carrier concentration of $1.5 \times 10^{13} \text{ cm}^{-2}$ and an electron mobility of 1170 cm^2/Vs . Device fabrication started with mesa isolation using Cl_2 plasma in an inductively-coupled-plasma reactive ion etch (ICP-RIE) system. Ohmic contacts were formed by rapid thermal annealing of evaporated Ti/Al/Ti/Au at 860°C for 30 s. Using on-wafer transfer length measurement (TLM) patterns, the ohmic contact resistance was typically measured to be $\sim 0.35 \Omega/\text{mm}$. Mushroom-shaped gates (Ni/Au) with gate-length (L_g) of 0.25 μm were fabricated using electron-beam lithography. The devices had a gate width of 100 μm and a source drain spacing of 2 μm .

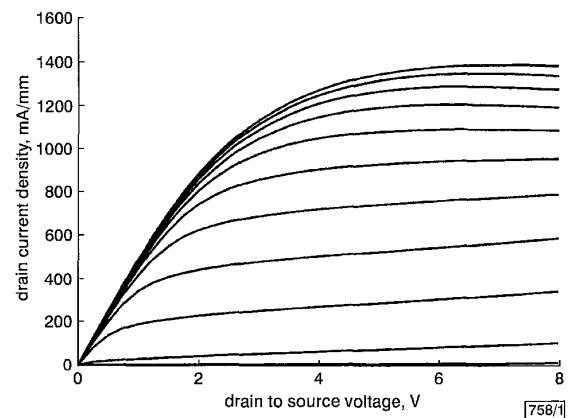


Fig. 1 DC I_D - V_{DS} characteristics of $0.25 \times 100 \mu\text{m}$ AlGaIn/GaN HEMT on sapphire substrate

Gate bias was swept from 2 to -8 V in steps of -1 V

The DC measurements were carried out using an HP4145B semiconductor parameter analyser. Fig. 1 shows the typical drain current-voltage (I_D - V_{DS}) characteristics of a device. The gate was biased from 2 to -8 V in steps of -1 V . The devices exhibited a maximum drain current density of 1.39 A/mm at a gate bias of 2 V and a drain bias of 7 V. To our knowledge this is the highest value ever reported for GaN-based HEMTs on a sapphire substrate. The DC transfer characteristics are shown in Fig. 2. The drain was biased at 5 V. A peak extrinsic transconductance (g_m) of 216 mS/mm was measured at $V_{gs} = -6.6 \text{ V}$ and $V_{ds} = 5 \text{ V}$.

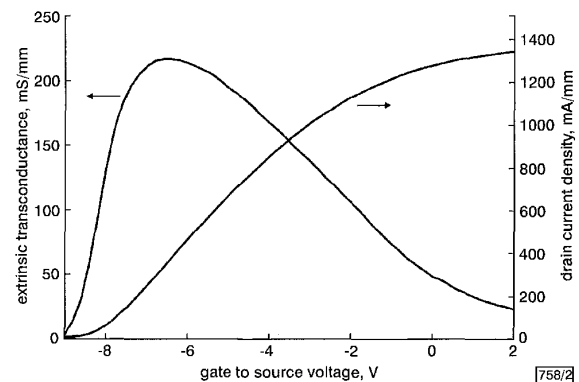


Fig. 2 DC transfer characteristics of $0.25 \times 100 \mu\text{m}$ AlGaIn/GaN HEMT on sapphire substrate

Drain bias was 5 V

RF measurements were carried out on-wafer using an HP8510C network analyser in the 1 to 50 GHz range. Fig. 3 shows the

short-circuit current gain ($|h_{21}|$) and maximum available power gain (G_{Amax}) derived from on-wafer S -parameters measurements against frequency. The values of unity gain cutoff frequency (f_T) and maximum frequency of oscillation (f_{max}) were determined by extrapolation of the $|h_{21}|$ and G_{Amax} data at -20 dB/decade. At a drain bias of 10 V and a gate bias of -6.7 V, an f_T of 67 GHz and f_{max} of 136 GHz were obtained, which to the best of the authors' knowledge are again the highest data ever reported for 0.25 μ m gate-length GaN-based FETs.

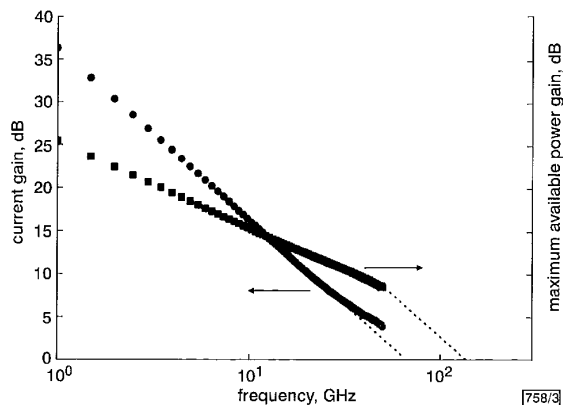


Fig. 3 Short-circuit current gain ($|h_{21}|$) and maximum available power gain (G_{Amax}) of typical 0.25×100 μ m AlGaIn/GaN HEMT on sapphire substrate

Device was biased at $V_{DS} = 10$ V and $V_{GS} = -6.7$ V

In summary, we have presented 0.25 μ m gate-length, MBE-grown high current density (1.39 A/mm) AlGaIn/GaN HEMTs on sapphire substrates. The measured values of f_T of 67 GHz and f_{max} of 136 GHz are the highest reported data for 0.25 μ m gate-length GaN-based HEMTs.

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Measurement of starting torque in surface tension self-assembly of microstructures

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The torque available at the start of out-of-plane rotation in surface tension powered self-assembly of microstructures is measured by deformation of an elastic spring. Values confirm to theoretical estimates and show that surface tension torque can overwhelm the gravitational counter-torque for realistic components in the microstructure size regime.

Surface tension powered self-assembly is a method of constructing three-dimensional microstructures [1, 2]. Movable parts are fabricated flat by surface patterning and sacrificial etching, and rotated out of the wafer plane by a surface tension torque obtained by heating small pads of meltable material linking moving parts to the substrate. The technique has been demonstrated using metal (Pb : Sn solder) [3 - 6], glass (BPSG) [7] and polymer (thick photoresist) [8] as the meltable material, and single-crystal silicon [3, 7], bonded silicon [8], polysilicon [4] and electroplated Ni [3] and Cu [6] as the mechanical material.

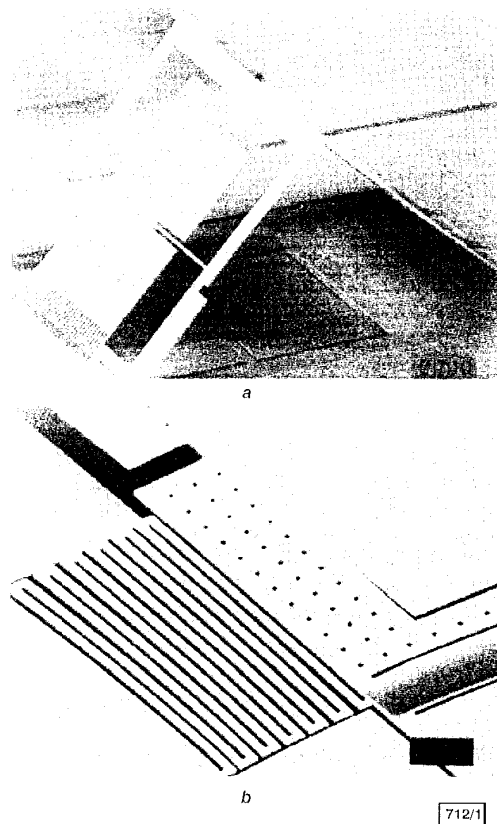


Fig. 1 Self-assembled 3D optical torsion mirror scanner and torque gauge

a Torsion mirror scanner
b Torque gauge

Surprisingly large structures (e.g. the torsion mirror scanner shown in Fig. 1a, whose frame has a height of 500 μ m) can be assembled in this way. Physical considerations suggest that because of its advantageous size scaling, surface tension force (which scales as dimension) should overcome elastic forces (dimension squared) and self-weight (dimension cubed) if the size of a structure is sufficiently reduced. To identify the limits of surface tension self-assembly, it is important to measure the torque available. This can be done with an elastic torque gauge, as we now show.

Fig. 2 shows the geometry, which consists of two self-assembling parts on a 1 mm baseline. Normally, the surface tension torque would cause these to rotate in opposite directions through 45° , until a mechanism is engaged to prevent further movement